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AN

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10036388	01/07/2002	257		2814	

****APPLICANTS:** Kishida Takenobu, Tada Shinya, Ikeda Atsushi, Harada Takeshi,
Sugihara Kohei.

****CONTINUING DATA VERIFIED:**

none, PC

**** FOREIGN APPLICATIONS VERIFIED:**

JAPAN 2001-178107 06/13/2001

yes, PC

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners initials <i>PC</i>		ATTORNEY DOCKET NO 740819-710
TITLE : Semiconductor device and method for fabricating the same		

U.S. DEPT. OF COM. PAT. & TM. FPO 436 (Rev. 12/94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE			
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